

**IN THE CLAIMS**

Please amend the claims as follows:

1. (Currently Amended) A system for partitioning and loading data in a low-powered communication device, the system comprising:

a general computing subsystem;

a modem computing subsystem in communication with the general computing subsystem;

a shared memory module for receiving a binary data, wherein the shared memory module is accessed by the general computing subsystem and the modem computing subsystem independently;

a first clock operable to provide the general computing system access to the shared memory module;

a second clock operable to be selectively activated by the general computing subsystem to the shared memory module to permit use of the shared memory module by the modem computing subsystem; and

a clock and power control unit disposed in the modem computing subsystem and operable to gate clocking within the modem computing subsystem and to the shared memory module when modem functionality is not enabled.

~~a shared memory module for receiving a binary data, wherein the shared memory module may be accessed by the general computing subsystem and the modem computing subsystem independently, wherein the general computing subsystem accesses the shared memory module using the first clock and wherein the general computing subsystem selectively activates the second clock to the shared memory module to permit use of the shared memory module by the modem computing subsystem.~~

2. (Original) The system of claim 1 wherein the modem computing subsystem controls data processing in accordance with wireless communication protocols.

3. (Original) The system of claim 2 wherein the modem computing subsystem further comprises a mobile station wireless modem.

4. (Original) The system of claim 1 wherein the general computing subsystem further comprises a nonvolatile memory that stores information for generating the data.

5. (Original) The system of claim 4 wherein the general computing subsystem loads the data into the shared memory module.

6. (Original) The system of claim 4 wherein the general computing subsystem generates the data from compressed information stored in the nonvolatile memory.

7. (Currently Amended) A portable wireless communication device, the device comprising:

a nonvolatile memory;

a general computing subsystem having access to the nonvolatile memory;

a modem computing subsystem selectively enabled and disabled by the general computing subsystem; ~~and~~

a first shared memory module independently accessible by the general computing subsystem and the modem computing subsystem, the first shared memory module being selectively enabled and disabled by the general computing subsystem; ~~and wherein;~~

a first binary image that is loaded in the first shared memory module from the nonvolatile memory by the general computing subsystem when selectively enabled, and ~~the first binary image loaded in the first shared memory module is accessible~~ accessed by the modem computing system to configure the modem computing subsystem; ~~and~~

a clock and power control unit in the modem computing subsystem and operable to gate clocking within the modem computing subsystem and to the shared memory module when modem functionality is not enabled.

8. (Currently Amended) The ~~system device~~ of claim 7 wherein the first binary memory image comprises mobile station modem code sufficient to permit the modem computing subsystem to establish a wireless communication link and monitor a paging channel.

9. (Currently Amended) The ~~system device~~ of claim 8, wherein the modem computing subsystem and the first shared memory module are enabled when the computing

subsystem starts to monitor the paging channel, and the modem computing subsystem and the first shared memory module are disabled when not engaged in wireless communication.

10. (Currently Amended) The ~~system~~ device of claim 8, further comprising a second shared memory module, wherein the second shared memory module is independently accessible by the general computing subsystem and the modem computing subsystem, wherein the second shared memory module can be disabled by the general computing subsystem to save power, and wherein a second binary memory image is loaded in the second shared memory module from the nonvolatile memory by the general computing subsystem.

11. (Currently Amended) The ~~system~~ device of claim 10, wherein the second binary memory image contains the mobile station modem code sufficient to operate a traffic channel.

12. (Currently Amended) The ~~system~~ device of claim 11, wherein the second shared memory module is activated when the modem computing subsystem operates a traffic channel, and the second memory module is deactivated to save power when ceasing to operate the traffic channel.

13-20. (Canceled)

21. (New) The system of claim 1 wherein the general computing system further comprises a general system processor and a power management unit, wherein the power management unit provides clocking to the general computing subsystem processor and the shared memory module.

22. (New) The system of claim 21 wherein the modem computing subsystem further comprises a modem subsystem processor, and wherein the clock and power control unit is further operable to gate clocking to the modem subsystem processor and to the shared memory module when modem functionality is not enabled.

23. (New) The system of claim 1 wherein the shared memory module further comprises at least one arbitration block operable to receive unrelated clock signals from the general computing subsystem and the modem computing subsystem.

24. (New) The system of claim 23 wherein the shared memory module further comprises at least one Dynamic Random Access Memory (DRAM), each DRAM corresponding to an arbitration block, wherein the DRAM is operable to conserve power when the memory is not clocked.

25. (New) The device of claim 7 wherein the general computing system further comprises a general system processor and a power management unit, wherein the power management unit provides clocking to the general computing subsystem processor and the shared memory module.

26. (New) The device of claim 25 wherein the modem computing subsystem further comprises a modem subsystem processor, and wherein the clock and power control unit is further operable to gate clocking to the modem subsystem processor and to the shared memory module when modem functionality is not enabled.

27. (New) The device of claim 10 wherein the first and second shared memory modules further each comprise at least one arbitration block operable to receive unrelated clock signals from the general computing subsystem and the modem computing subsystem.

28. (New) The device of claim 27 wherein the first and second shared memory modules further each comprise at least one Dynamic Random Access Memory (DRAM), each DRAM corresponding to an arbitration block, wherein the DRAM is operable to conserve power when the memory is not clocked.